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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): NOGUCHI et al.

Serial No.: 09/825,946

Filed: April 5, 2001

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND
FABRICATION METHOD FOR SEMICONDUCTOR
INTEGRATED CIRCUIT DEVICE

Group: 2811

Examiner: H. Vu

#16
Ext. 1mw
J. McMillan
5/31/03RECEIVED
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PETITION FOR EXTENSION OF TIME

Commissioner For Patents
Washington, D.C. 20231

May 15, 2003

Sir:

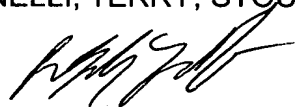
In the matter of the above-identified application, Applicants hereby respectfully petition for an extension of time to permit filing a response within a three-month time period subsequent to expiration of the shortened statutory period set in the outstanding Office Action mailed November 20, 2002. A credit card payment form in the amount of \$520.00 (\$410.00 was submitted on April 16, 2003 covering the first two months fee) to cover the required fee for the requested extension of time is attached hereto.

It is respectfully requested that any shortage in the fee be charged to the deposit account of Antonelli, Terry, Stout & Kraus, LLP, Account No. 01-2135 (501.39932X00) and any excess fees credited to that account.

Respectfully submitted,

ANTONELLI, TERRY, STOUT & KRAUS, LLP

By


Ralph T. Webb
Reg. No. 33,047

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